



#2325-03  
Hosy/Refers  
PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

in re application of

Hiroataka ANAKANO et al.

Serial No. 09/855,723

GROUP 2812

Filed May 16, 2001

Examiner Unknown

SYSTEM OF MANUFACTURING SEMICONDUCTOR INTEGRATED CIRCUIT

RECEIVED  
AUG 20 2001  
TC 2800 MAIL ROOM

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
Washington, D.C. 20231

Sir:

In compliance with Rules 1.97 and 1.98, and in fulfillment of the duty of disclosure under Rule 1.56, the accompanying documents, copies of which are attached to this statement, are made of record on the enclosed sheet.

A concise explanation of the relevance of these items is that these references were discovered during any searches they or their client had made, or that they were considered in the preparation of the application.

Under the provisions of 37 CFR 1.97(e), the undersigned hereby certifies that no item contained in this Information Disclosure Statement was known to any individual designated in §1.56(c), more than three months prior to the filing of this Statement.

Respectfully submitted,

YOUNG & THOMPSON

By

Robert J. Patch  
Attorney for Applicant  
Registration No. 17,300  
745 South 23rd Street  
Arlington, VA 22202  
Telephone: 703/521-2997

RECEIVED  
JUL 17 2002  
TECHNOLOGY CENTER 2800

August 16, 2001